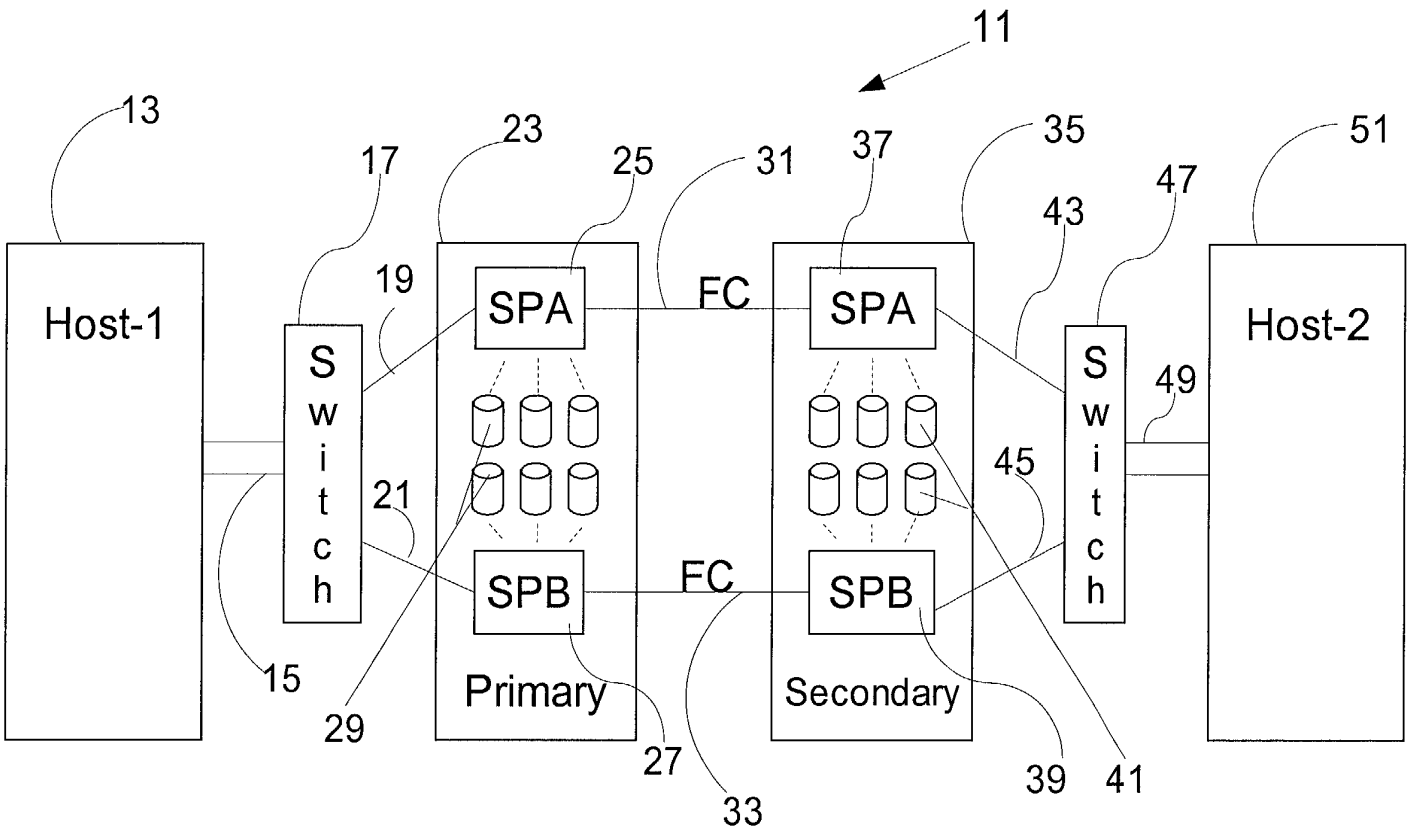
11
13
17
23
25
31
37
35
51
47
43
19
Host-1
Switch
SPA
FC
SPA
Switch
Host-2
49
45
21
SPB
FC
SPB
15
29
Primary
Secondary
27
33
39
41

Figure 1

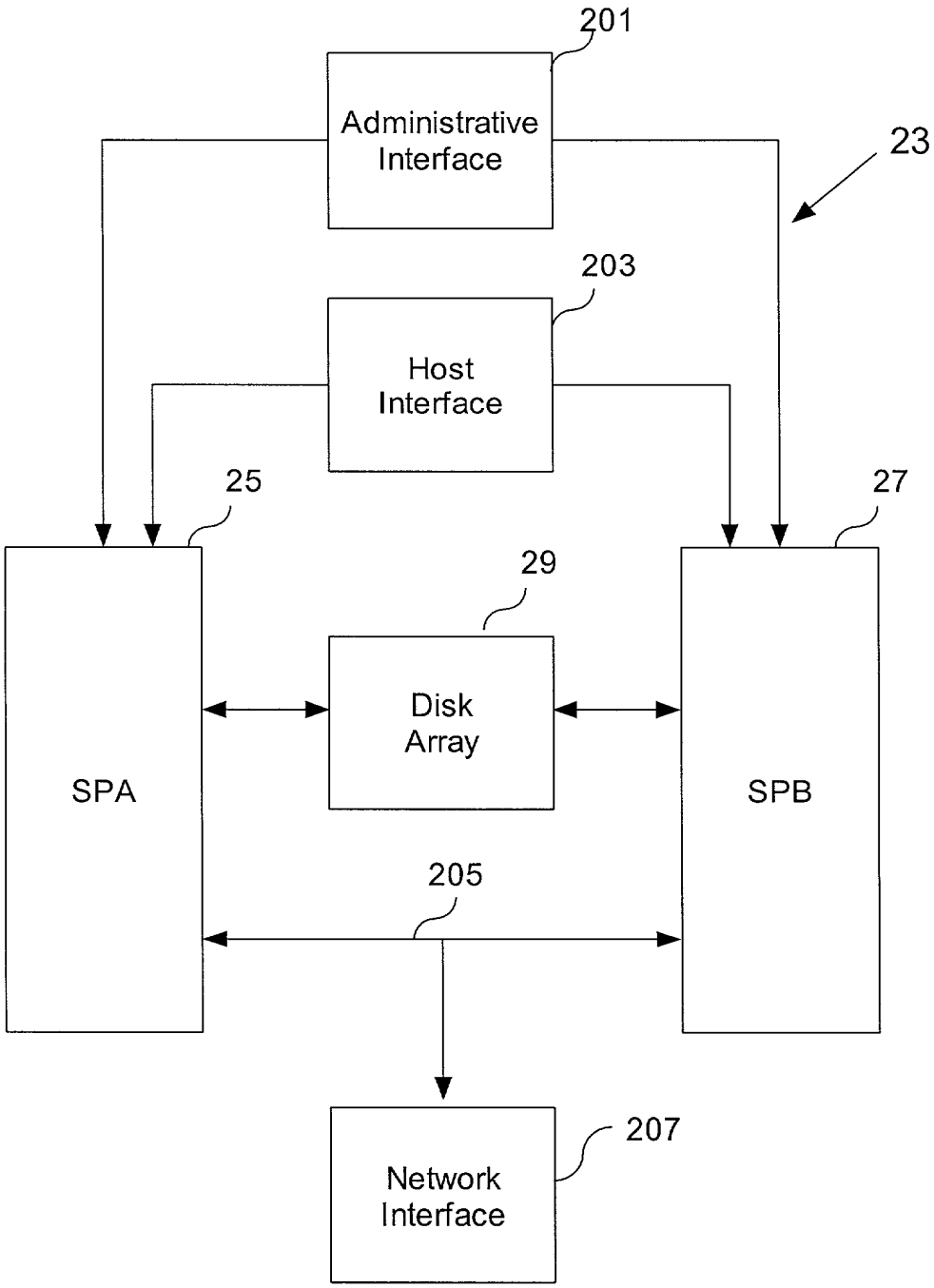
201
Administrative Interface
23
203
Host Interface
25
27
29
Disk Array
SPA
SPB
205
207
Network Interface

Figure 2

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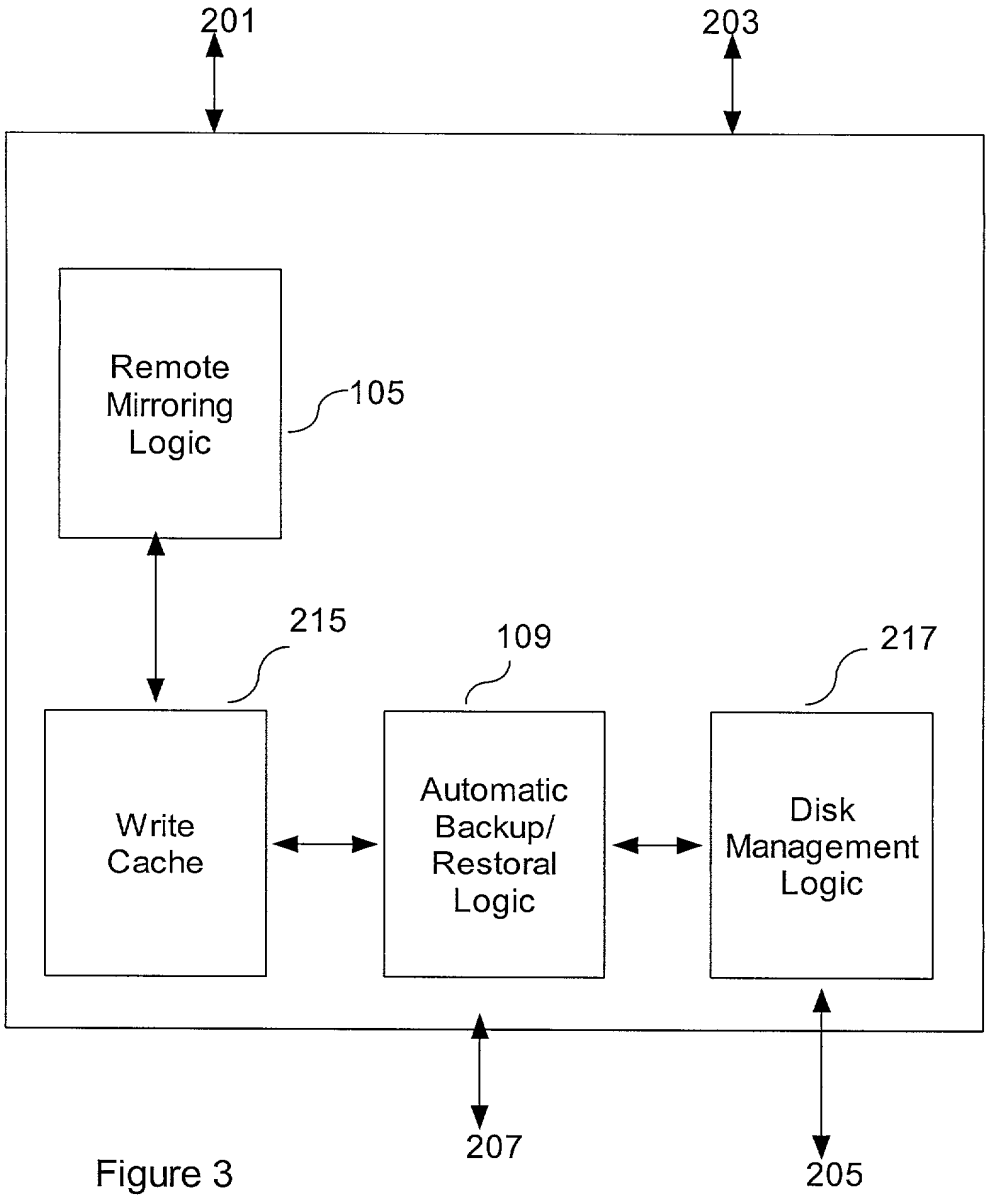

Figure 3

Patent Application Publication

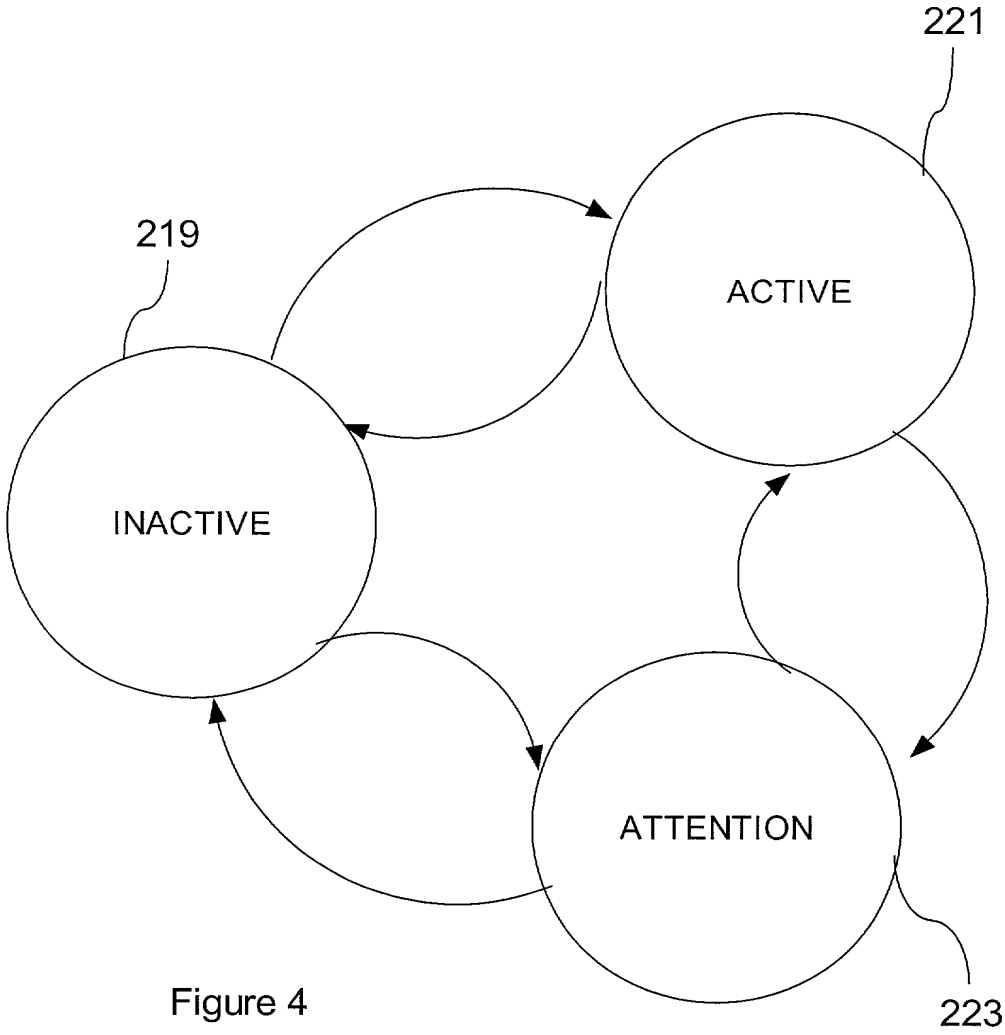

Figure 4

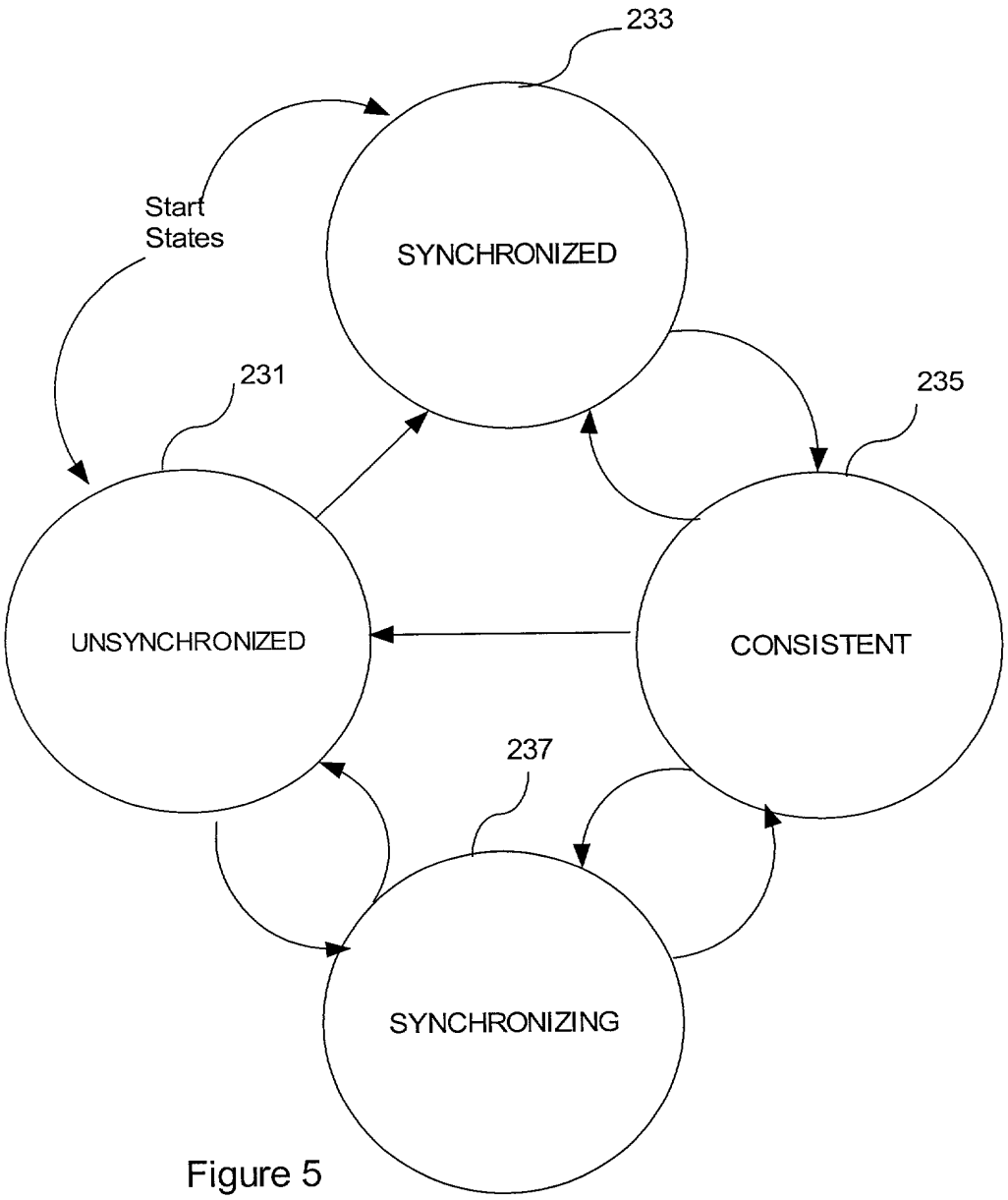
233
Start
States
SYNCHRONIZED
231
235
UNSYNCHRONIZED
CONSISTENT
237
SYNCHRONIZING

Figure 5

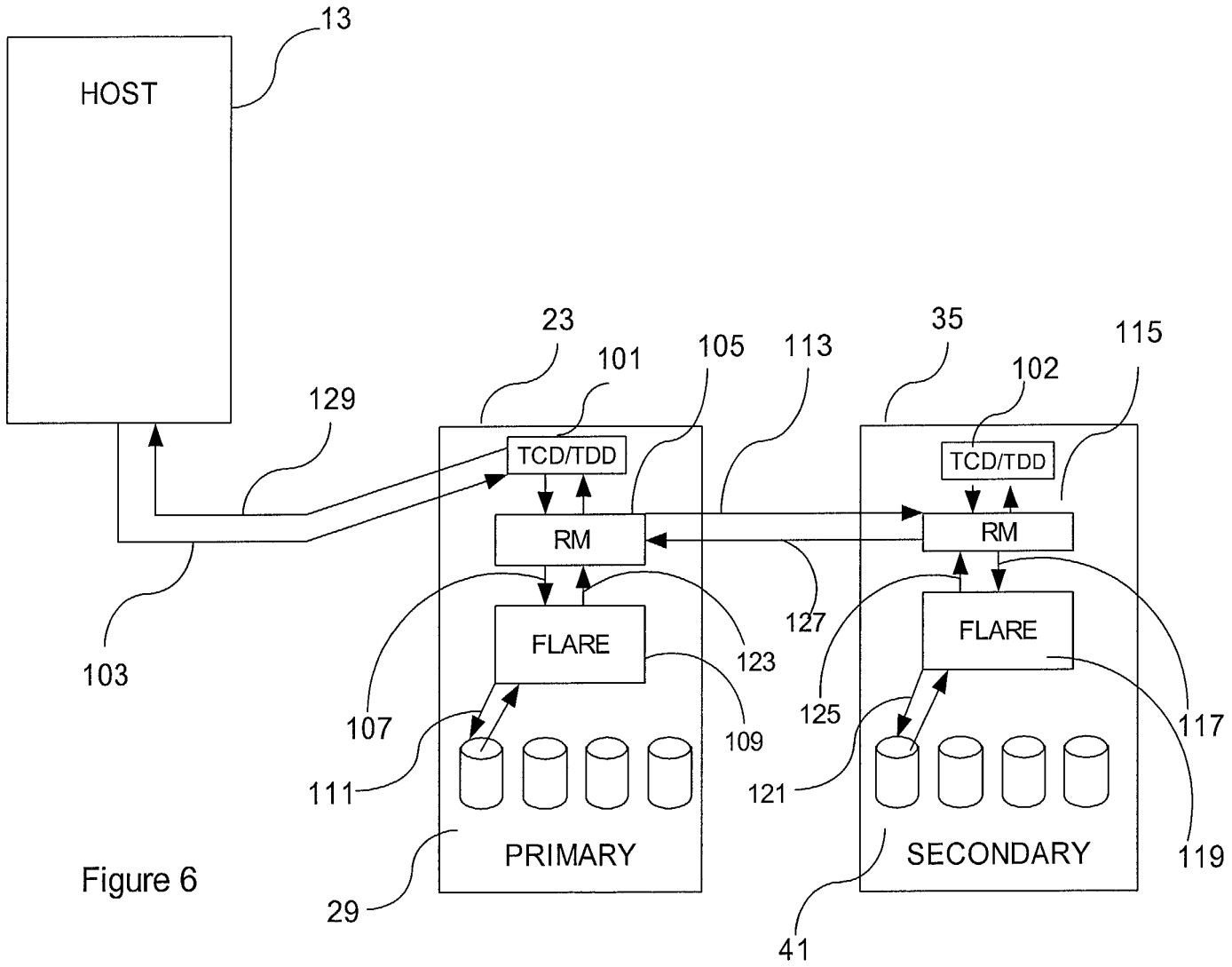
13
HOST
129
103
23
101
105
113
35
102
115
TCD/TDD
TCD/TDD
RM
RM
FLARE
FLARE
127
123
107
109
111
125
121
117
119
PRIMARY
SECONDARY
29
41

Figure 6

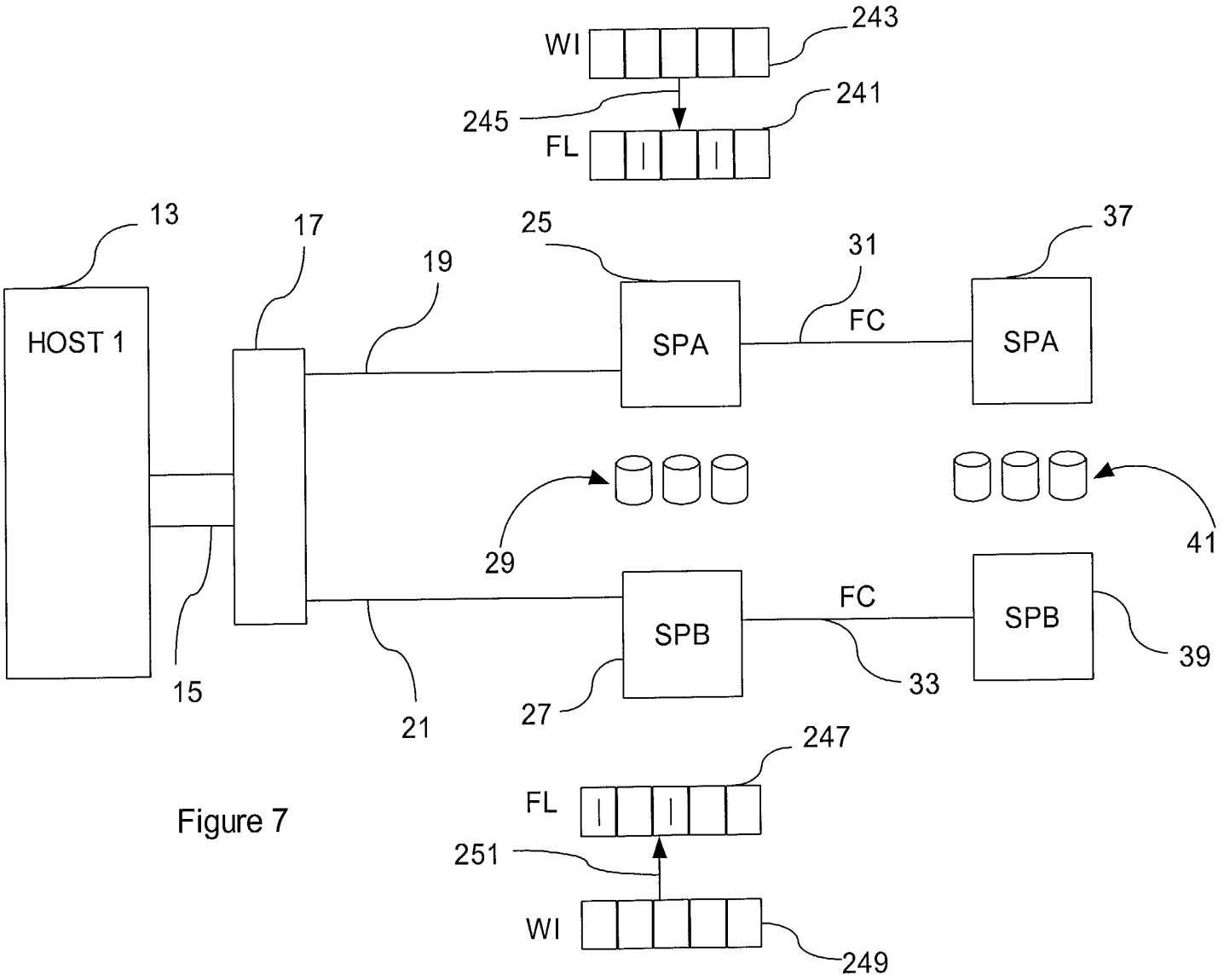
WI
243
245
241
FL
13
17
19
25
31
37
HOST 1
SPA
FC
SPA
29
41
15
21
SPB
FC
SPB
27
33
39
247
FL
251
WI
249

Figure 7

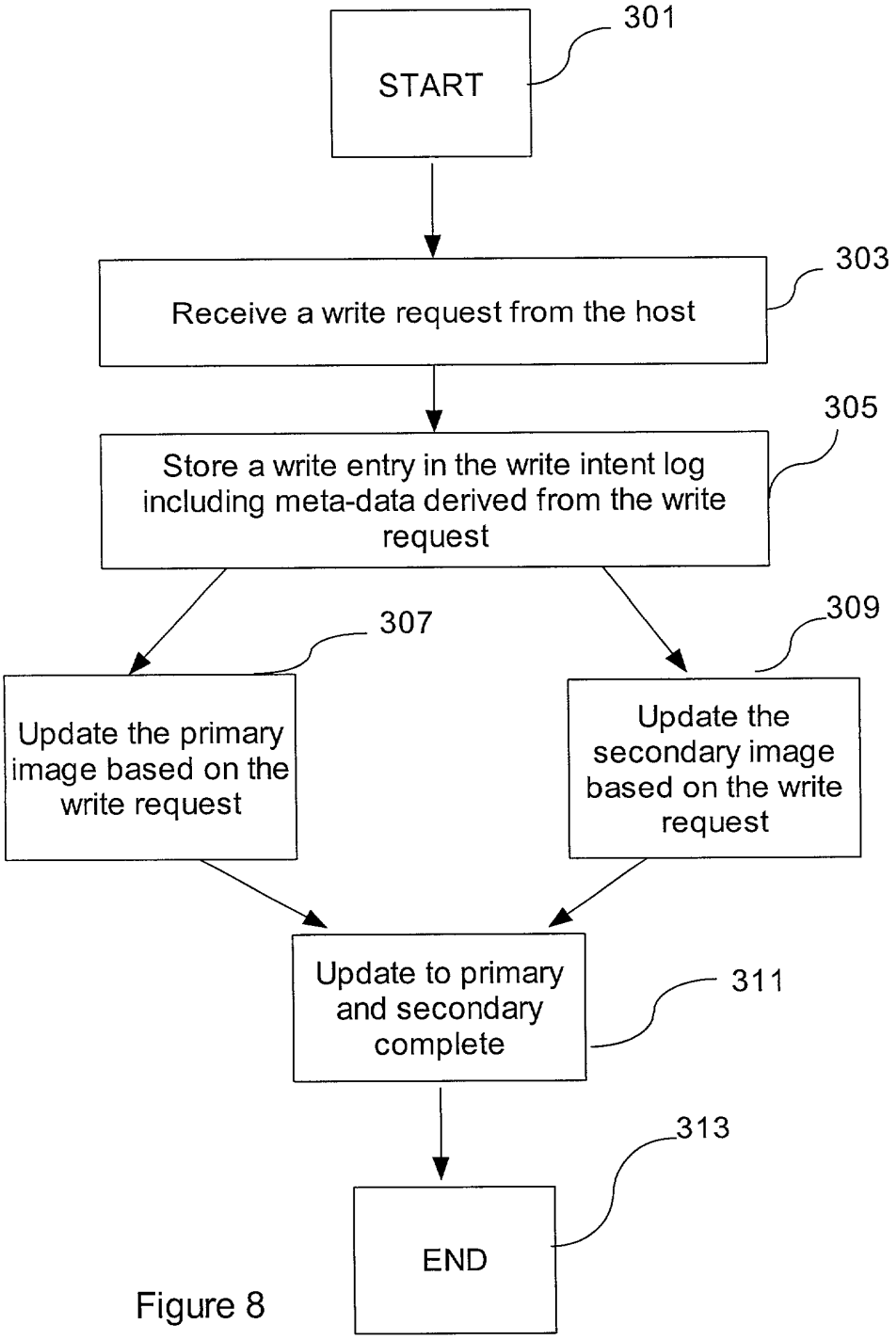

Figure 8

EMC-3.026CP8

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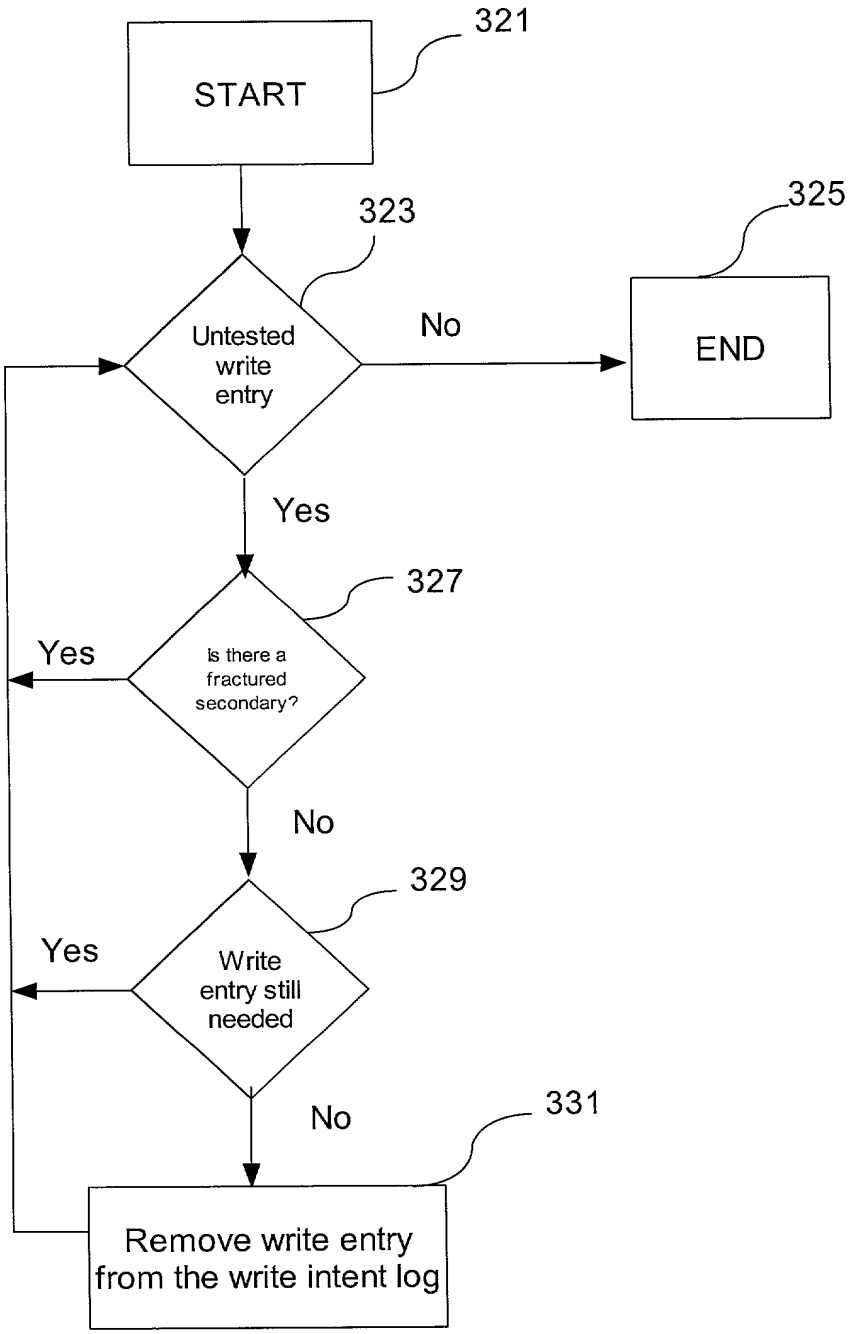

Figure 9

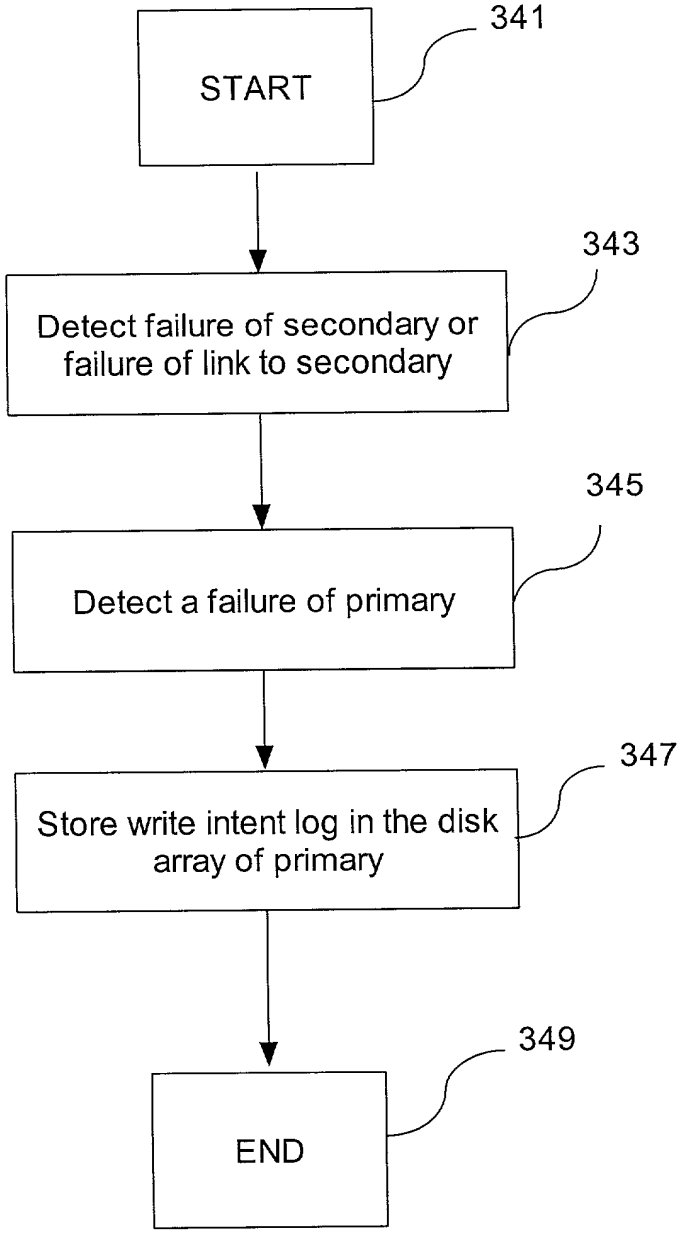
341
START
343
Detect failure of secondary or failure of link to secondary
345
Detect a failure of primary
347
Store write intent log in the disk array of primary
349
END

Figure 10

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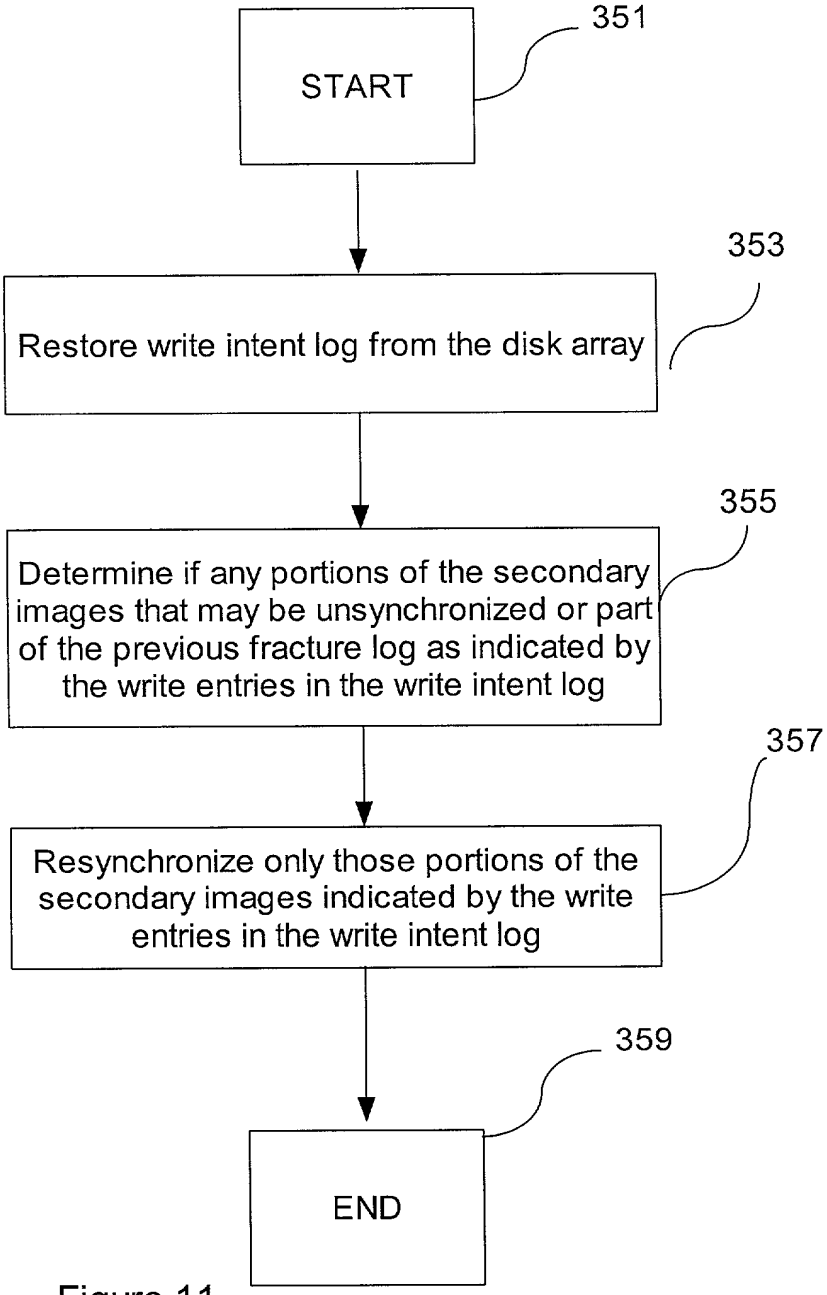

Figure 11